

JUMPERS ON MAIN COMPUTER

D14.125

DSPC 153 Main single-board computer

- 1. Memory address field
 - S6: 1-2,3-4,5-6 0-64 kbytes
- 2. Memory type
 - S10: 1-3,5-6,9-10 EPROM 2764
 - S20: 1-3,5-6,9-10 EPROM 2764
 - S2: 2-4 Normal operation
- 3. Back-up voltage
 - S5: 1-2,3-5,4-6 None
- 4. Access time
 - S1: 7-8 2 wait-states
- 5. Mode
 - S2: 7-8 Normal
 - S8: 1-2 Normal
 - S9: 1-2 Normal
- 6. Inhibit-N S11: 1-2 shall be removed

Attention! The jumpers which are not marked must be placed according to the actual options which are included in the system.

Other jumper groups are not to be used

The pin marked "1" on the short side of the strap group is the first pin.

D14.125

DSPC 150 Main single-board computer

- 1. Memory address field
 - S6: 1-2 Memories on the board
 - S13: 2-3,5-6,8-9 0-64 kbytes
 - 11-12,14-15,17-18
- 2. Memory type
 - S10: 1-3,7-8,11-12 EPROM 2764
 - S20: 1-3,7-8,11-12 EPROM 2764
- 3. Back-up voltage
 - S5: 1-2,3-5,4-6 None
- 4. Access time
 - S12: 3-4 2 wait-states
- 5. Mode
 - S2: 3-4 Normal operation
 - X9: 1-2 Normal operation
- 6. Inhibit-N S11: 1-2 shall be removed

5	Jumpers DSPC 150	R1502	04	50
	DSPC 153 del.	R1165		
4	SHEET REDRAWN	SQ 84	19	
3	INHIBIT-N ADDED	R108	SQ 83	4,6
Revised		App'd Year Week		

DESIGNED BY	LINDQVIST
TRACED BY	JKEM
APPROVED BY	QUICK/T/S
CIRCUIT DIAGRAM	CONTROL SYSTEM IRB 90S/2
ASEA	JKCP 83 20

6704	100	BEA
4	5	

JUMPERS ON MEMORY BOARDS

'014.121'

DSMB 124	PROM/RW memory board
1. Memory address field	
S1: 2-3, 4-5, 8-9, 11-12	<input checked="" type="checkbox"/> 256k - 384 k bytes
2. Memory type	
S10: 2-4, 5-6	<input checked="" type="checkbox"/> EPROM 2764
S11: 1-2, 3-4	<input checked="" type="checkbox"/> RW 6116
S12: 1-2, 3-4	<input checked="" type="checkbox"/> RW 6116
3 Back-up voltage	
S2: 3-4, 5-6, 9-10, 11-12, 13-14, 15-16	<input checked="" type="checkbox"/> External and separate RW
4 Access time	
S4: 1-2	<input checked="" type="checkbox"/> Quick acknowledge activated

'014.117'

DSMB 125	PROM/RW memory board
1. Memory address field	
S1: 1-2, 4-5, 8-9, 11-12	<input checked="" type="checkbox"/> 384k - 512 k bytes
2. Memory type	
S10: 2-4, 5-6	<input checked="" type="checkbox"/> EPROM 2764
S11: 1-2, 3-4	<input checked="" type="checkbox"/> RW 6116
S12: 1-2, 3-4	<input checked="" type="checkbox"/> RW 6116
3 Back-up voltage	
S2: 3-4, 5-6, 9-10, 11-12, 13-14, 15-16	<input checked="" type="checkbox"/> External and separate RW
4. Access time	
S4: 1-2	<input checked="" type="checkbox"/> Quick acknowledge activated

Bildkort

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6	JUMPER S2:3-4, add RT 627	185 31
5	OPTION FOR DSMB125 del	1/186 50
	New sheet RT 502	Agreed

Designed by: Hardgeird
 Drawn by: Petterson
 U.C.

CIRCUIT DIAGRAM
 CONTROL SYSTEM IRB 90S/2
 JISAK 84 50

6704 100-BEA
 4,5
 5

JUMPERS ON AXIS CONTROL BOARD

D14.141

DSQC 104/DSQC 123 R/D and D/A converter Axis 1,2,3

1. I/O address
 S1: 1-2, 5-6, 8-9, 11-12 # 41
 14-15, 17-18, 19-20, 23-24
Bygging hexadecimal

2. Measuring channels
 X4: 21-23, 22-24 Axis 1, 2, 3

D14.145

DSQC 104/DSQC 123 R/D and D/A converter Axis 4, 5, 6

1. I/O address
 S1: 1-2, 5-6, 7-8, 11-12 # 45
 14-15, 17-18, 19-20, 23-24

2. Measuring channels
 X4: 21-23, 24-26 Axis 4, 5
 X4: 21-23, 22-24 Axis 4, 5, 6

D14.149

DSQC 104/DSQC 123 R/D and D/A converter Axis 7, 8, 9 (Option)

1. I/O address
 S1: 1-2, 5-6, 8-9, 10-11 # 49
 14-15, 17-18, 19-20, 23-24

2. Measuring channels
 X4: 23-25, 24-26 Axis 7 (Option)
 X4: 21-23, 24-26 Axis 7, 8 (Option)
 X4: 21-23, 22-24 Axis 7, 8, 9 (Option)

D14.129

DSQA 110 Axis slave computer

1. I/O address
 S1: 1-2, 5-6, 8-9, 10-11 # 29
 14-15, 16-17, 20-21, 23-24

2. Memory type
 S6: 1-2, 3-4 RW 6116
 S5: 1-3, 5-6 EPROM 2764

3. Access time
 S4: 3-4 1 wait-state
 S3: 1-2 0 wait-state area 3

4. Mode
 S2: 7-9 Normal
 S7: 1-2 Normal

D14.137

DSQC 117 Resolver feed and one D/A (OPTION)

1. I/O address
 S1: 1-2, 4-5, 7-8, 11-12 # 37
 13-14, 16-17, 20-21, 23-24

addr. vns 45

1 0
 2 0
 4 0
 8 0

5 } *entails offset*

1 0
 2 0
 4 0
 8 0

4 } *10 tabs suffix*

DESIGN CHECKED BY: []	DATE: []	REV: []	NO. OF SHEETS: []
DRAWN BY: []	SCALE: []	TITLE: []	PROJECT: []
APPROVED BY: []	DATE: []	REV: []	NO. OF SHEETS: []
CIRCUIT DIAGRAM CONTROL SYSTEM IRB 90S12 JKP 83 20 ASEA			6704 100-BEA 5 6

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JUMPERS ON I/O BOARDS

D14.133

D5MC 110 Floppy-disc interface

- I/O address
 S1: 1-2, 4-5, 8-9, 11-12 **153**
 13-14, 16-17, 20-21, 23-24
- Density, writepulse adjustment and mode
 S2: 1-2, 3-4 Normal
 S3: 1-2 Normal

D14.153

D5DX 110 I/O-mini

- I/O address
 S1: 1-2, 4-5, 8-9, 11-12 **153**
 13-14, 17-18, 19-20, 23-24
- Mode
 X3: 37-38, 39-40 Normal

D14.161

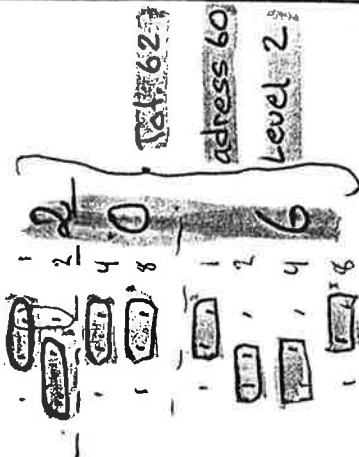
D5CA 121 PD-bus communication 1)

- I/O address and interrupt level
 S1: 4-5, 7-8, 11-12 **153** *OBS! ATT 1-2 avbrøtt AS till Level 2*
 13-14, 17-18, 19-20, 23-24
 S1: 1-2 Level 2 *Avbrøtt hiva*
- Mode
 S2: 1-2, 3-4, 5-6, 7-8, 9-10 Normal

D14.161

D5CA 114 Asynchronous communication module (Option)

- I/O address and interrupt level
 S1: 8-9, 11-12, 14-15, 16-17, 19-20, 23-24 **160** *OBS!*
 S1: 2-3, 4-5 Level 2 *(162)*



1) D5CA 121 INCLUDED ONLY WHEN D5PC 153 IS USED

D14.157

D5QC 121 Monitorboard

- Memory address field (8k byte)
 S1: 2-3, 5-6, 8-9, By placing the jumpers in different ways it is possible to choose any 8k byte field within 0-2M byte.
 11-12, 14-15, 16-17,
 20-21, 23-24

6	D5QC 121 inf	RI 594	B5 31
5	D5CA 121 DEL	RI 165	B4 50
4	SHEET REDRAWN	SQ B4 19	Actual Year Work

Through checked by
LINDOVIST
 Primary checked by
JKEM
 Checked by
QUICKITS

CIRCUIT DIAGRAM
 CONTROL SYSTEM IRB 90S/2
ASEA
 JKCP B3 20

Rev. No. Sheet
 6704 100 - BEA
 Rev. No. Sheet
 6
 Conf.

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Åskott

JUMPERS ON I/O BOARD

'D14.169'

DSDX 110 I/O - mini }
 DSDO 110,120,130,131,140 Digital outputs } (OPTION)
 DSDI 110,120,130,140 Digital inputs }
 DSAI 120 2) Analog inputs }
 DSAO 110 Analog outputs }

1. I/O address
 S1: 1-2, 5-6, 8-9, 10-11 69
 14-15, 16-17, 19-20, 23-24

2. Test light diodes on DSAI 120
 S2: 1-2 Off

3. Mode on DSDX 110, DSDI 110, 120, 130, 140
 X3: 37-38, 39-40 Normal

4. Function on DSAO 110
 S101, S201, S301, S401 Normal
 1-2, 3-4, 5-6, 7-8

5. Mode on DSAO 110 1)
 S102, S202, S302, S402:
 1-2, 5-6 0 - ±10V
 1-2, 7-8 0 - ±10mA
 1-3, 7-8 0 - ±20mA

6. Mode on DSAO 110
 Solder strap W106, W206, W306, W406
 connected 0 - ±10V
 not connected 0 - ±10mA
 connected 0 - ±20mA

'D14.165'

DSDX 110 I/O - mini }
 DSDO 110,120,130,131,140 Digital outputs } (OPTION)
 DSDI 110,120,130,140 Digital inputs }
 DSAI 120 2) Analog inputs }
 DSAO 110 Analog outputs }

1. I/O address
 S1: 1-2, 5-6, 7-8, 11-12 65
 14-15, 16-17, 19-20, 23-24

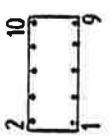
2. Test light diodes on DSAI 120
 S2: 1-2 Off

3. Mode on DSDX 110, DSDI 110, 120, 130, 140
 X3: 37-38, 39-40 Normal

4. Function on DSAO 110
 S101, S201, S301, S401 Normal
 1-2, 3-4, 5-6, 7-8

5. Mode on DSAO 110 1)
 S102, S202, S302, S402:
 1-2, 5-6 0 - ±10V
 1-2, 7-8 0 - ±10mA
 1-3, 7-8 0 - ±20mA

6. Mode on DSAO 110
 Solder strap W106, W206, W306, W406
 connected 0 - ±10V
 not connected 0 - ±10mA
 connected 0 - ±20mA



1) Strap group orientation for mode on DSAO 110

2) Straps on Terminal unit for DSAI 120 should be removed.

Make sure that the straps don't fall inside the cabinet.

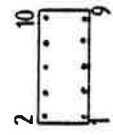
DESIGN CHECKED BY: [Signature]	DATE: 8/11/81	REV: 1	APPROVED BY: [Signature]	DATE: 8/11/81	REV: 1
DRAWN BY: [Signature]	CHECKED BY: [Signature]	DATE: 8/11/81	DESIGNED BY: [Signature]	DATE: 8/11/81	REV: 1
PROJECT: CONTROL SYSTEM IRB 90S/2		JOB NO: JKL P 63 20		SHEET NO: 7	
CIRCUIT DIA GRAM		6704 100 - BEA		REVISED SHEET	
ASEA		JKCP 63 20		7.5	

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JUMPERS ON I/O BOARD

'D14,173'

<p>DSDX 110 DSDO 110, 120, 130, 131, 140 DSDI 110, 120, 130, 140 DSAO 110 DSQC 114</p> <p>I/O - mini Digital outputs Digital inputs Analog outputs Welding timer</p> <p>(OPTION)</p>	<p>1. I/O address S1: 1-2, 4-5, 8-9, 11-12 13-14, 16-17, 19-20, 23-24</p> <p>2. Made on DSDX 110, DSDI 110, 120, 130, 140 X3: 37-38, 39-40</p> <p>3. Function on DSAO 110 S101, S201, S301, S401 1-2, 3-4, 5-6, 7-8</p> <p>4. Made on DSAO 110 S102, S202, S302, S402 1-2, 5-6 1-2, 7-8 1-3, 7-8</p> <p>5. Made on DSAO 110 Solder strap W106, W206, W306, W406 connected <input type="checkbox"/> 0 - ±10V not connected <input type="checkbox"/> 0 - ±10 mA connected <input type="checkbox"/> 0 - ±20 mA</p>
<p>DSDX 110 DSDO 110, 120, 130, 131, 140 DSDI 110, 120, 130, 140 DSQC 114</p> <p>I/O - mini Digital outputs Digital inputs Welding timer</p> <p>(OPTION)</p>	<p>1. I/O address S1: 1-2, 4-5, 7-8, 11-12 13-14, 16-17, 19-20, 23-24</p> <p>2. Made on DSDX 110, DSDI 110, 120, 130, 140 X3: 37-38, 39-40</p>



1) Strap group orientation for mode on DSAO 110

<p>Drawn by: Grom No. _____</p> <p>Checked by: _____</p> <p>Design checked by: _____</p> <p>Rev. No. _____</p>	<p>DATE: _____</p> <p>SCALE: _____</p> <p>PROJECT: _____</p> <p>WORK NO.: _____</p>	<p>DATE: _____</p> <p>SCALE: _____</p> <p>PROJECT: _____</p> <p>WORK NO.: _____</p>	<p>DATE: _____</p> <p>SCALE: _____</p> <p>PROJECT: _____</p> <p>WORK NO.: _____</p>
<p>6 DSDO 131 INTR RI 534 SQ 85 31</p> <p>4 SHEET REDRAWN SQ 84 19</p> <p>3 DSAO 110 JUMPERS ADD RI 147 SQ 83 46</p> <p>1 DSAO 110 A.D. D14,173 WAS D14,169 SQ 83 23</p>	<p>6704 100 - BEA</p>	<p>7.5</p>	<p>8</p>
<p>QUICKTITS</p>	<p>ASEA</p>	<p>JKCP 83 20</p>	<p>JKCP 83 20</p>
<p>DESIGN CHECKED BY: LINDQVIST</p> <p>DESIGN CHECKED BY: JKEM</p>	<p>CIRCUIT DIAGRAM</p>	<p>CONTROL SYSTEM IRB 90517</p>	<p>6704 100 - BEA</p>

Bildikon

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JUMPERS ON SERVO CONTROL BOARD

'022.149'
 YYT 102B Control board Axis 4
 1. Overload SI: 1-2 8A

'022.161'
 YYT 102B Control board Axis 5
 1. Overload SI: 1-2 8A

'022.173'
 YYT 102 B Control board Axis 6
 1. Overload SI: 1-2 8A

'022.113'
 YYT 102G Control board with tacho Axis 1
 1. Overload SI: 7-8 13A
 2. Servo adaptivity S2: 1-2 3-4 No Yes (option)

'022.125'
 YYT 102 F Control board with tacho Axis 2
 1. Overload SI: 7-8 13A

'022.137'
 YYT 102F Control board with tacho Axis 3
 1. Overload SI: 7-8 13A

Drawn by: []
 Checked by: []
 Year Week: []

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 Checked by: []
 Year Week: []

4	SHEDRAWN SERVO ADAPT ADD RI 755	S.O. 84 19	Applied Year Week
Drawing No.		Year Week	

Designed/checked by
 LINDQVIST
 Drawing checked by
 JKEM
 QUICKITS

CIRCUIT DIAGRAM
 CONTROL SYSTEM IRB 90 S 12
 JKCP 83 20

6704 100 -BEA	Rev No. Sheet
	Rev No. 8
	Count 9

JUMPERS ON DRIVE UNITS AND CONVERTER

D31.113

YYT 102A Control board with tacho External axis 7 (Option)

1. Overload

S1: 7-8

S1: 1-2

6,5A External small motor

8A External large motor

D31.125

YYT 102A Control board with tacho External axis 8 (Option)

1. Overload

S1: 7-8

S1: 1-2

6,5A External small motor

8A External large motor

D31.137

YYT 102A Control board with tacho. External axis 9 (Option)

1. Overload

S1: 7-8

S1: 1-2

6,5A External small motor

8A External large motor

D28.14

YXX 157A Voltage set rectifier board

1. Frequency

No jumper

S1: 1-2 and S2:1-2

50Hz

60Hz

Billikort

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Drawn by	Order no.
Design checked by	TID No.
Rev. no.	Year
Week	Com.

Drawn by	Form No.
Design checked by	Rev. no.
Year	Week
Com.	

Designed by
LINDQVIST
Quantity checked by
JKEM
Drawn by
QUICK/TS

CIRCUIT DIAGRAM
CONTROL SYSTEM IRB 90S/2
JKCP 8320

SHEET REDRAWN	SQ. 84 19
Applied Year	Week

Per No. Sheet	9
Per No. Sheet	10

6704 100 - BEA

ASEA

JKCP 8320

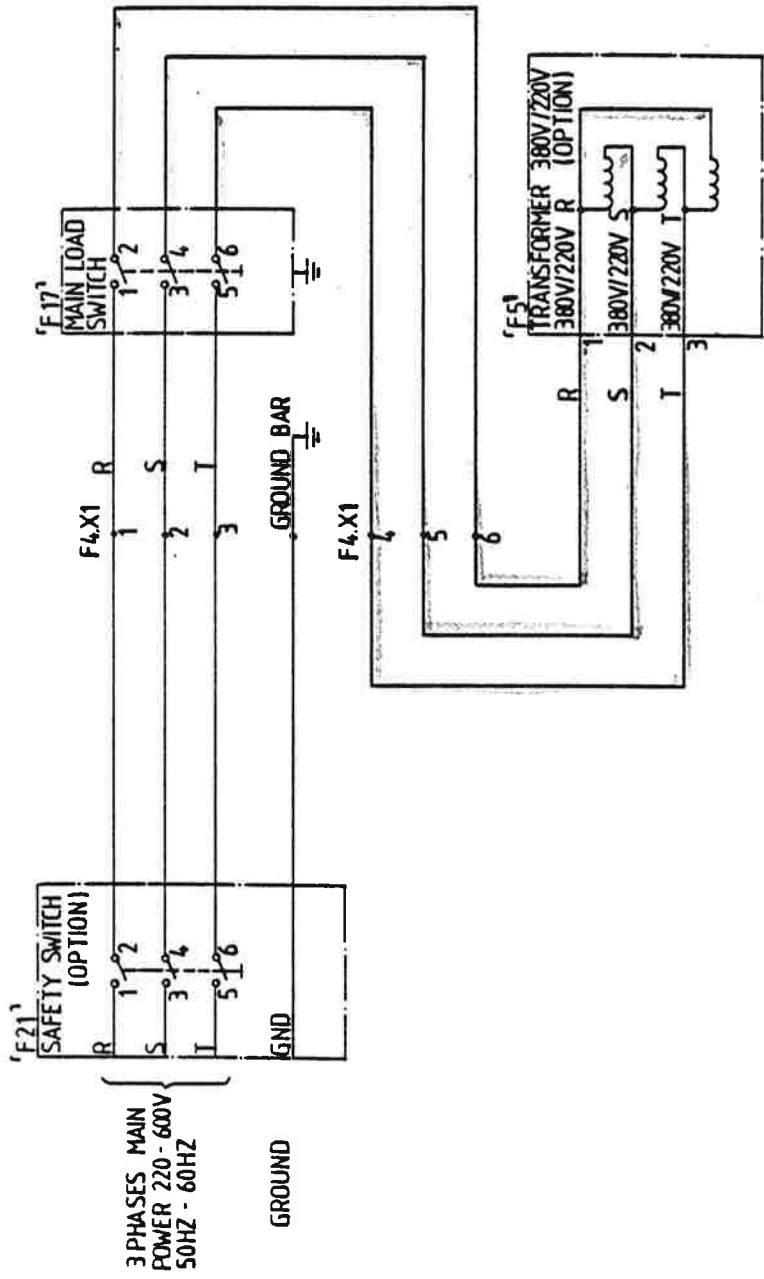
IRB 90S/2

CONTROL SYSTEM

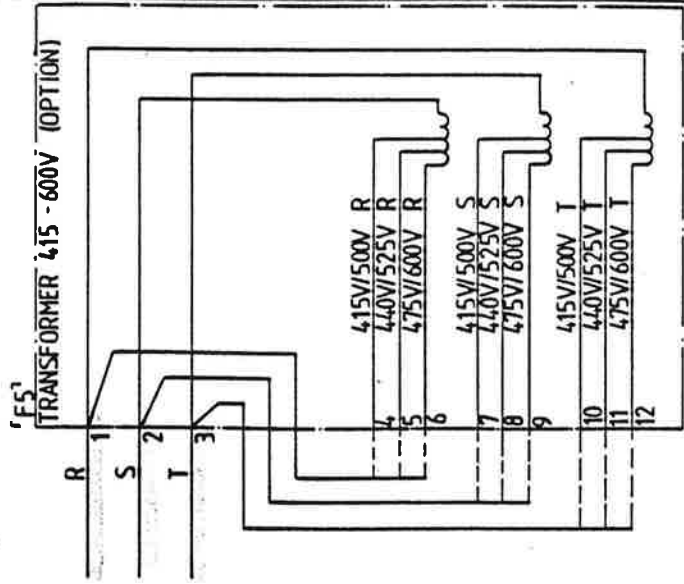
CIRCUIT DIAGRAM

MAIN POWER CONNECTION

Färs.p. vid tillslag
stand b 7
RUN



POWER VOLTAGE	CONNECTIONS
415V/500V	Byggingar 1-4, 2-7, 3-10
440V/525V	1-5, 2-8, 3-11
475V/600V	1-6, 2-9, 3-12



Drawn by: []
 Design checked by: []
 Date: []

KOT

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 Design checked by: []
 Date: []

0917 5338 AA (A) Rev 1: 1997

4 SHEET REDRAWN AND ADJ. SQ. 84, 19

QUICK/ITS

ASEA

CONTROL SYSTEM IRB 90S/2

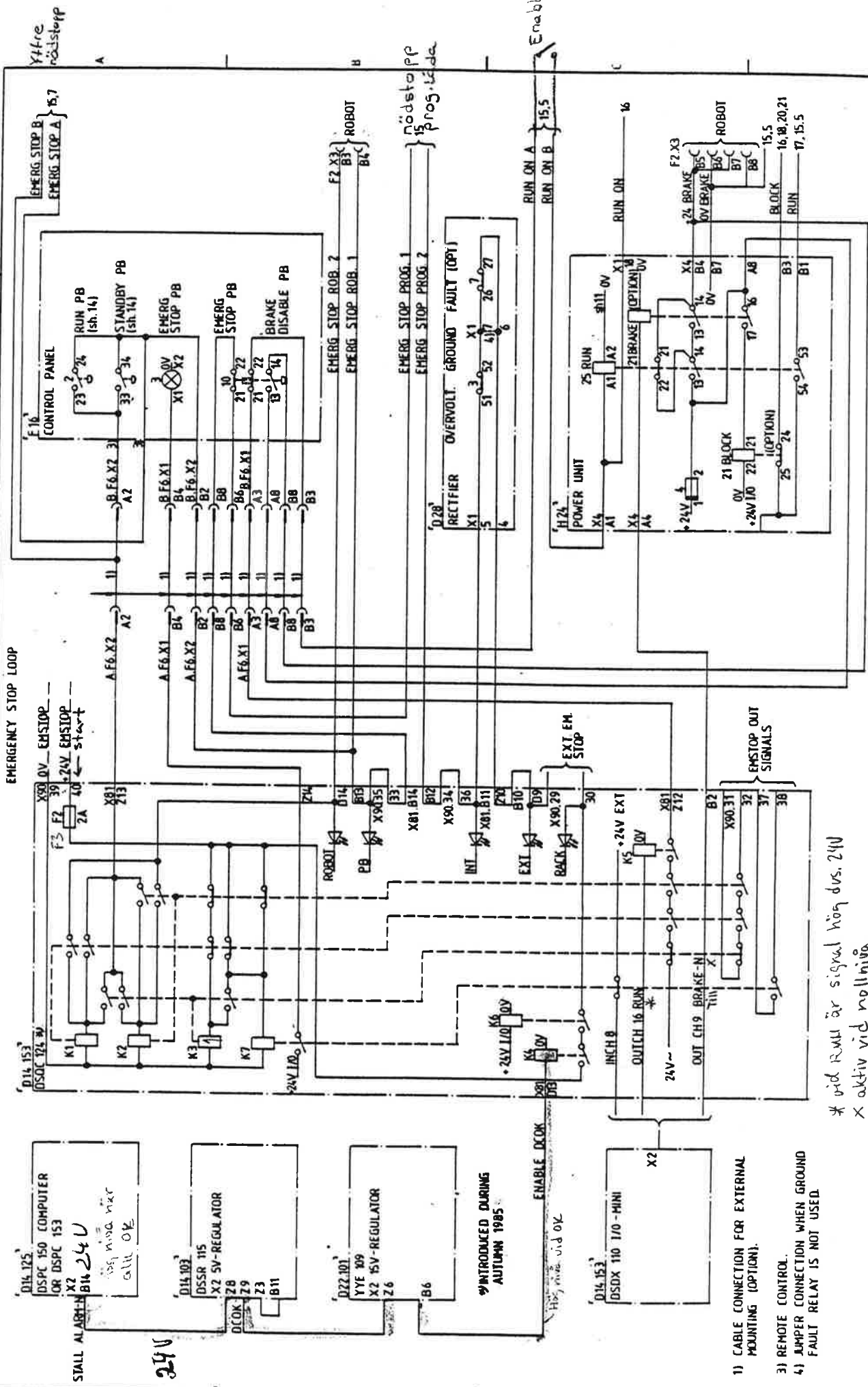
JKP 83 20

6704 100 - BEA

10

11





D14.125
 DSPC 150 COMPUTER
 OR DSPC 153
 X2
 B14 24V
 sig noga nax
 alla OK
 24V

D14.103
 DSSR 115
 X2 5V-REGULATOR
 DCOK Z8
 Z9
 Z3
 B11

D22.101
 YVE 109
 X2 15V-REGULATOR
 Z6
 B6

INTRODUCED DURING
 AUTUMN 1985

ENABLE DCOK
 H57 noga vid OK

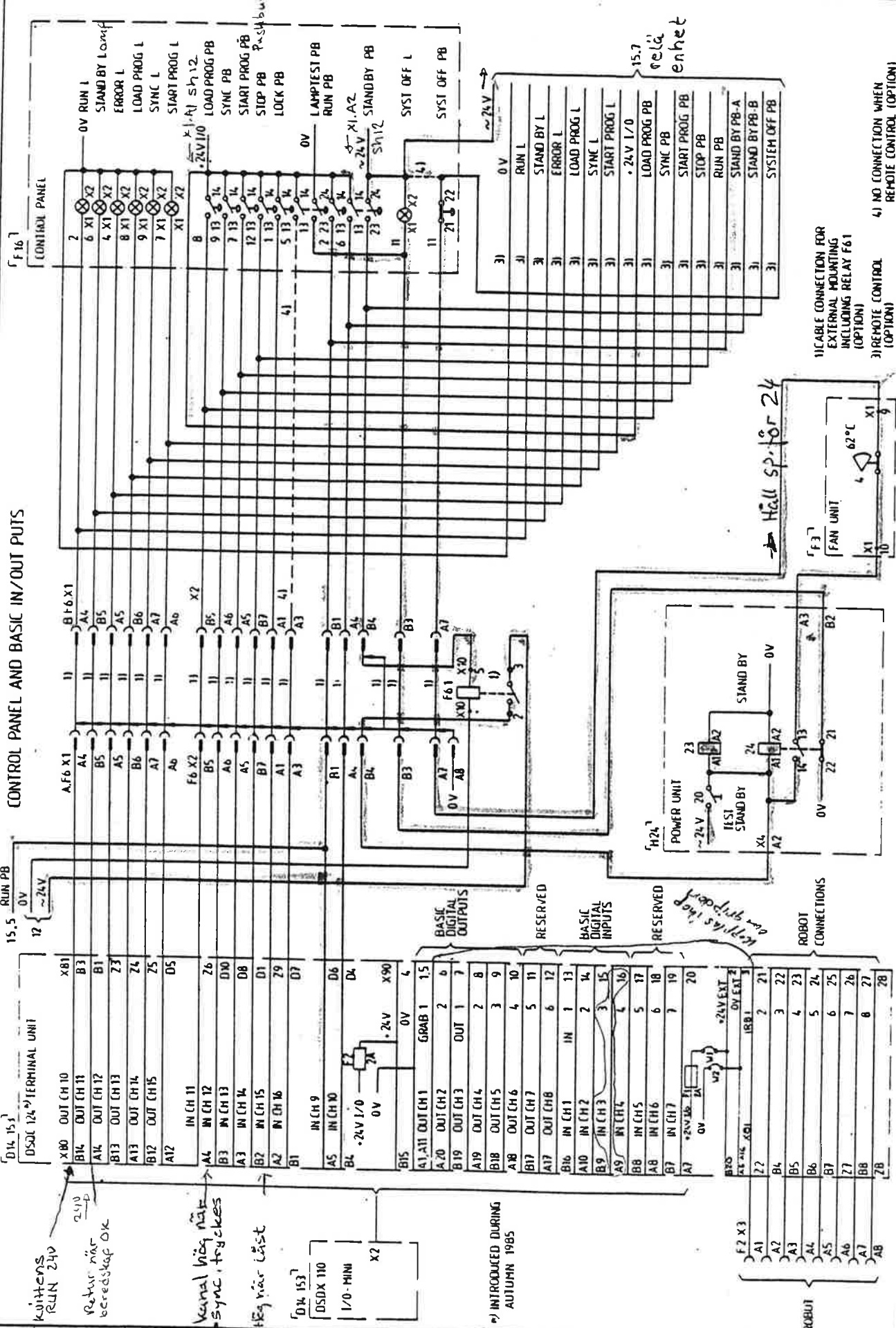
D14.153
 DSDX 110 1/0-MINI
 X2

- 1) CABLE CONNECTION FOR EXTERNAL MOUNTING (OPTION).
- 3) REMOTE CONTROL.
- 4) JUMPER CONNECTION WHEN GROUND FAULT RELAY IS NOT USED.

* vid runn är signal hög dvs. 24V
 X aktiv vid nollström

Drawn by	Year	Mod.	Year
Design checked by	Year	Mod.	Year
Approved by	Year	Mod.	Year
CIRCUIT DIAGRAM CONTROL SYSTEM IRB 90S/2			
ASEA			
Designed by	Year	Mod.	Year
Drawn by	Year	Mod.	Year
Checked by	Year	Mod.	Year
Approved by	Year	Mod.	Year
NY VERSION			
6704 100-BEA			
Version	Sheet	Sheet	Count
	13	13	135

CONTROL PANEL AND BASIC IN/OUT PUTS



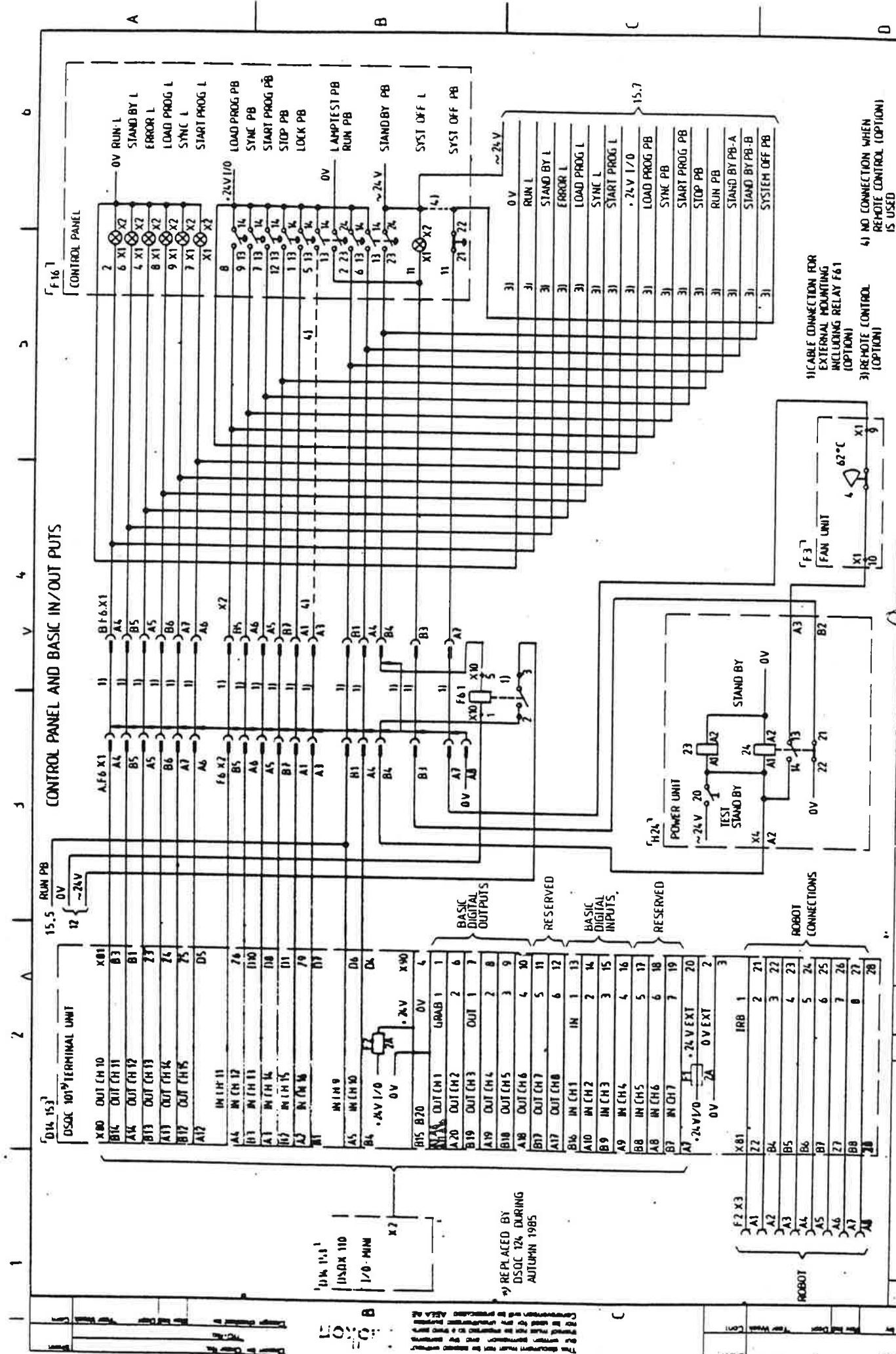
6	24V 1/0	Sh 34, 5 add	B5 31
4	DSOC 124	int. RI 592	SO B4 19
3	SH REPRAMM. AND. ADJ.	R 216, R 252	SO B3 46
3	F6 TADD	R 95	SO B3 46

DRAWN BY: [] FROM NO. []
 DESIGN CHECKED BY: [] YEAR WEEK: []
 DRAWN BY: [] FROM NO. []
 DESIGN CHECKED BY: [] YEAR WEEK: []
 APPROVAL YEAR WEEK: []

CIRCUIT DIAGRAM
 CONTROL SYSTEM
ASEA
 IRB 90S/2
 JKCP 83 20

6704 100-BEA
 Rev. No. Sheet 14
 Cont. 14.5

INTRODUCED DURING
 AUTUMN 1985

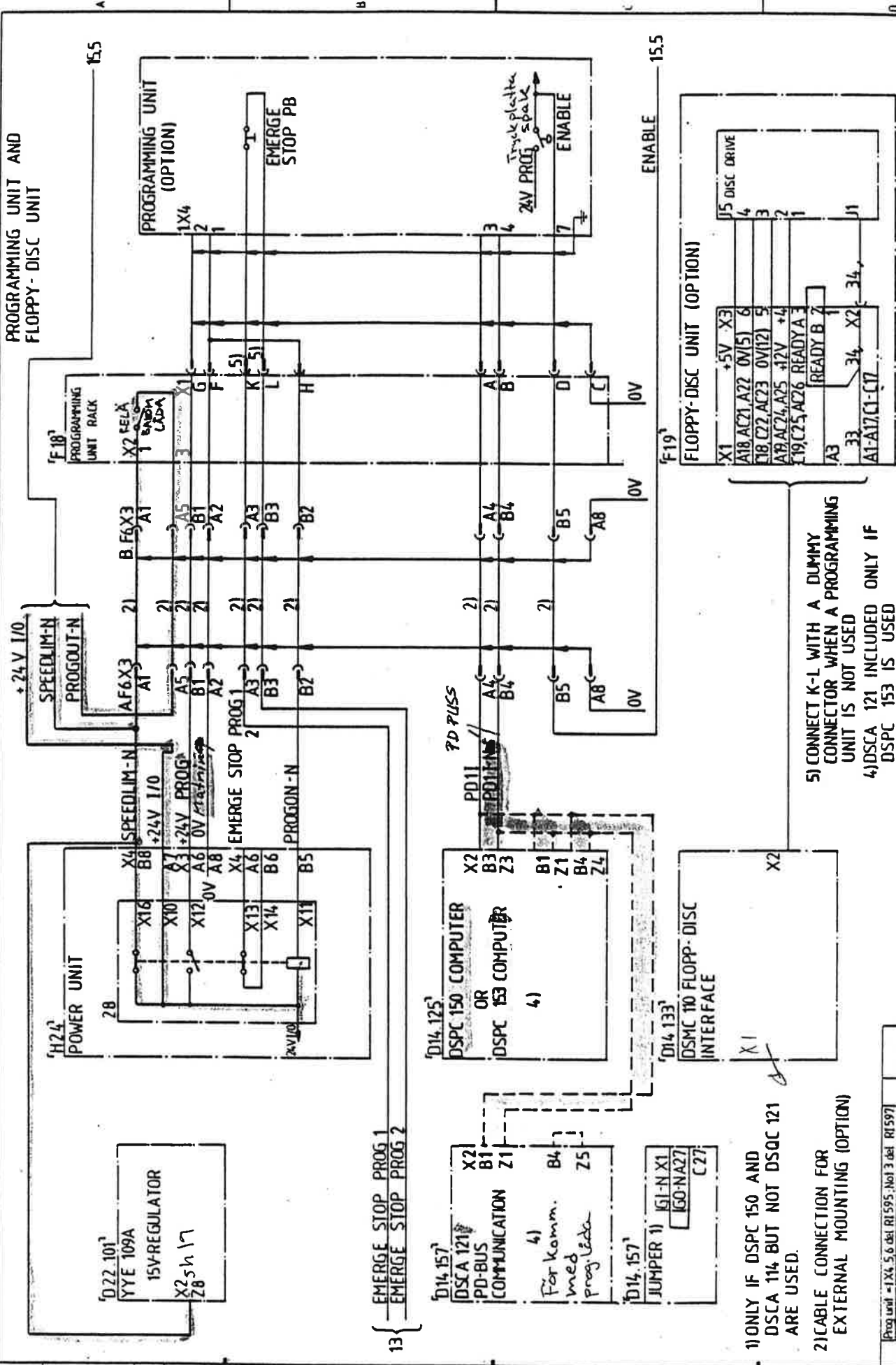


1) CABLE CONNECTION FOR EXTERNAL MOUNTING INCLUDING RELAY F61 (OPTION)
 2) REMOTE CONTROL (OPTION)
 3) NO CONNECTION WHEN REPHOTE CONTROL (OPTION) IS USED

CIRCUIT DIAGRAM	
CONTROL SYSTEM	IRB90S/2
ASEA	JSAK 85 31
Design Checked by	LINDQVIST
Drawn by	JIKEM
Appar Year/Week	
Rev. No. Revision	
6704 100-BA	15
14.5	15

*) REPLACED BY DSOC 124 DURING AUTUMN 1985

DSOC 110 1/0-MIN



PROGRAMMING UNIT AND FLOPPY-DISC UNIT

+24V I/O
SPEEDIM-N
PROGOUT-N

F18
PROGRAMMING UNIT RACK

F19
FLOPPY-DISC UNIT (OPTION)

ENABLE

15.5

15.5

ENABLE

EMERGE STOP PROG 1
EMERGE STOP PROG 2

DSQA 121
PD-BUS COMMUNICATION

DSQC 150 COMPUTER OR DSQC 153 COMPUTER

DSMC 110 FLOPPY-DISC INTERFACE

1) ONLY IF DSQC 150 AND DSQA 114 BUT NOT DSQC 121 ARE USED.

2) CABLE CONNECTION FOR EXTERNAL MOUNTING (OPTION)

5) CONNECT K-L WITH A DUMMY CONNECTOR WHEN A PROGRAMMING UNIT IS NOT USED
4) DSQA 121 INCLUDED ONLY IF DSQC 153 IS USED

Prog unit - 1X4.5.6 del RI 595 (Not 3 del RI 597)	B5 31
D14.14.9 moved to sh 22 - Sh 15.2 add	
D14.157 inlr dummy connector inlr RI 595	
Screened prog unit inlr RI 488	B4 50
EI set PD-bus del RI 507	
SH REDRAWN DSQC 150 INTRODUCED	SQ B4 19

Drawn by: Form NC	Sheet: 6
Checked by: Year Week	Appl Year Week
Design checked by: Year Week	Year Week
Design checked by: Year Week	Year Week

Approved by: JNDVIMST
Drawing checked by: JKEM
Quantity: JKCP B3 20

CIRCUIT DIAGRAM
CONTROL SYSTEM IRB 90S/2
In Use Date Year Week

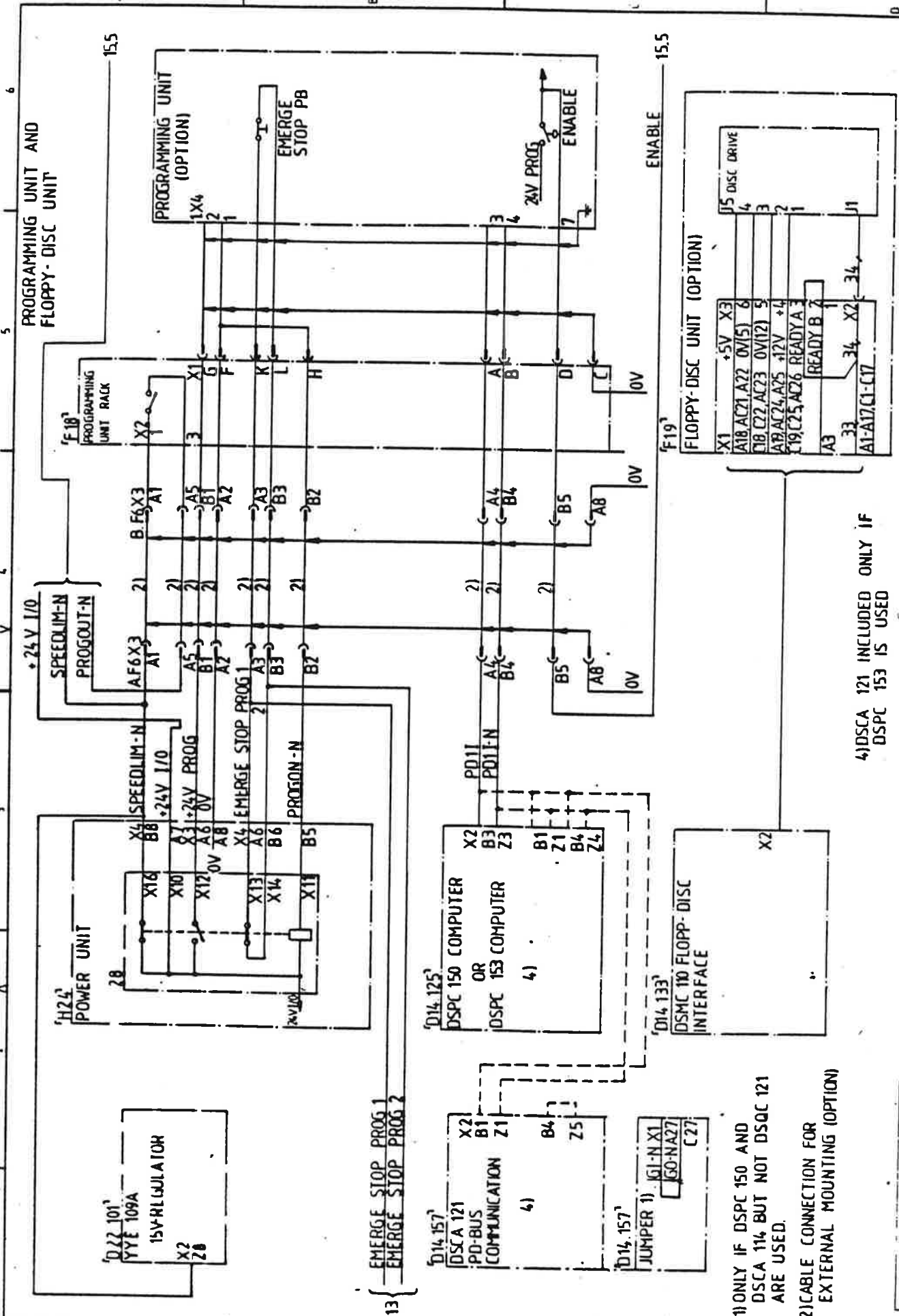
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Rev. Ind Sheet
Rev. Ind Sheet
15
15.2

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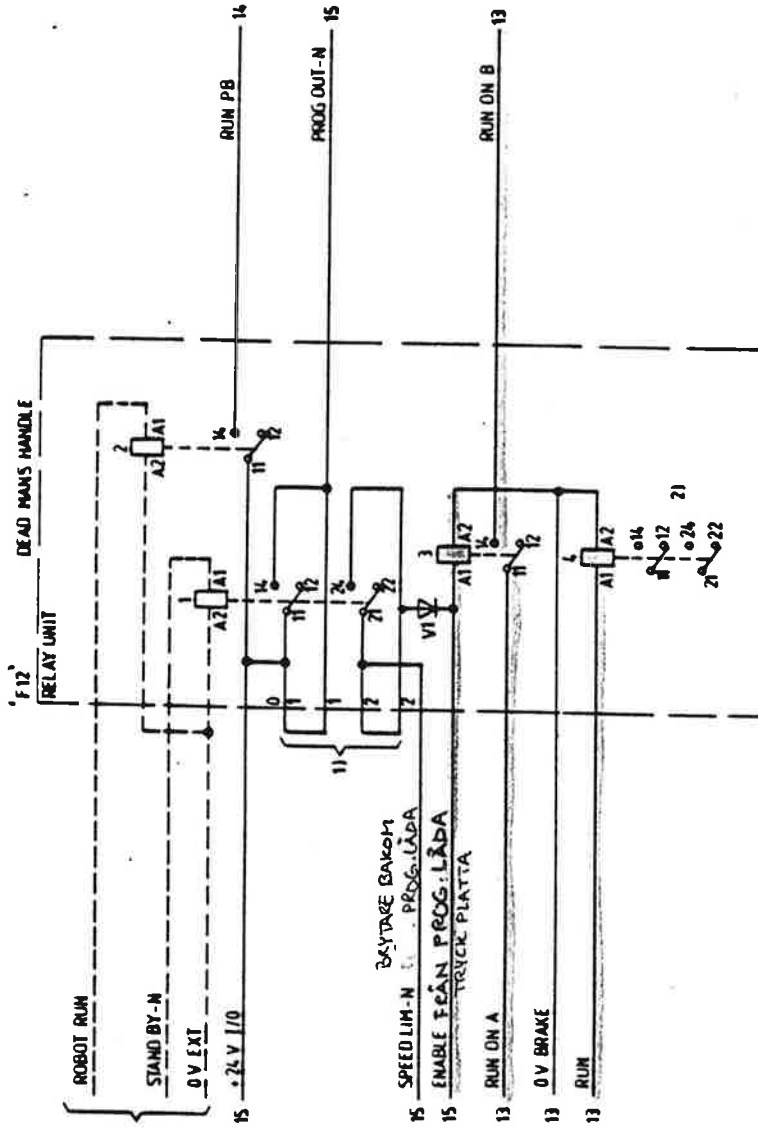
Lidkorf

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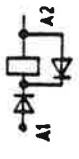


- 1) ONLY IF DSQC 150 AND DSQC 114 BUT NOT DSQC 121 ARE USED.
- 2) CABLE CONNECTION FOR EXTERNAL MOUNTING (OPTION)
- 4) DSQA 121 INCLUDED ONLY IF DSQC 153 IS USED

DEAD MANS HANDLE

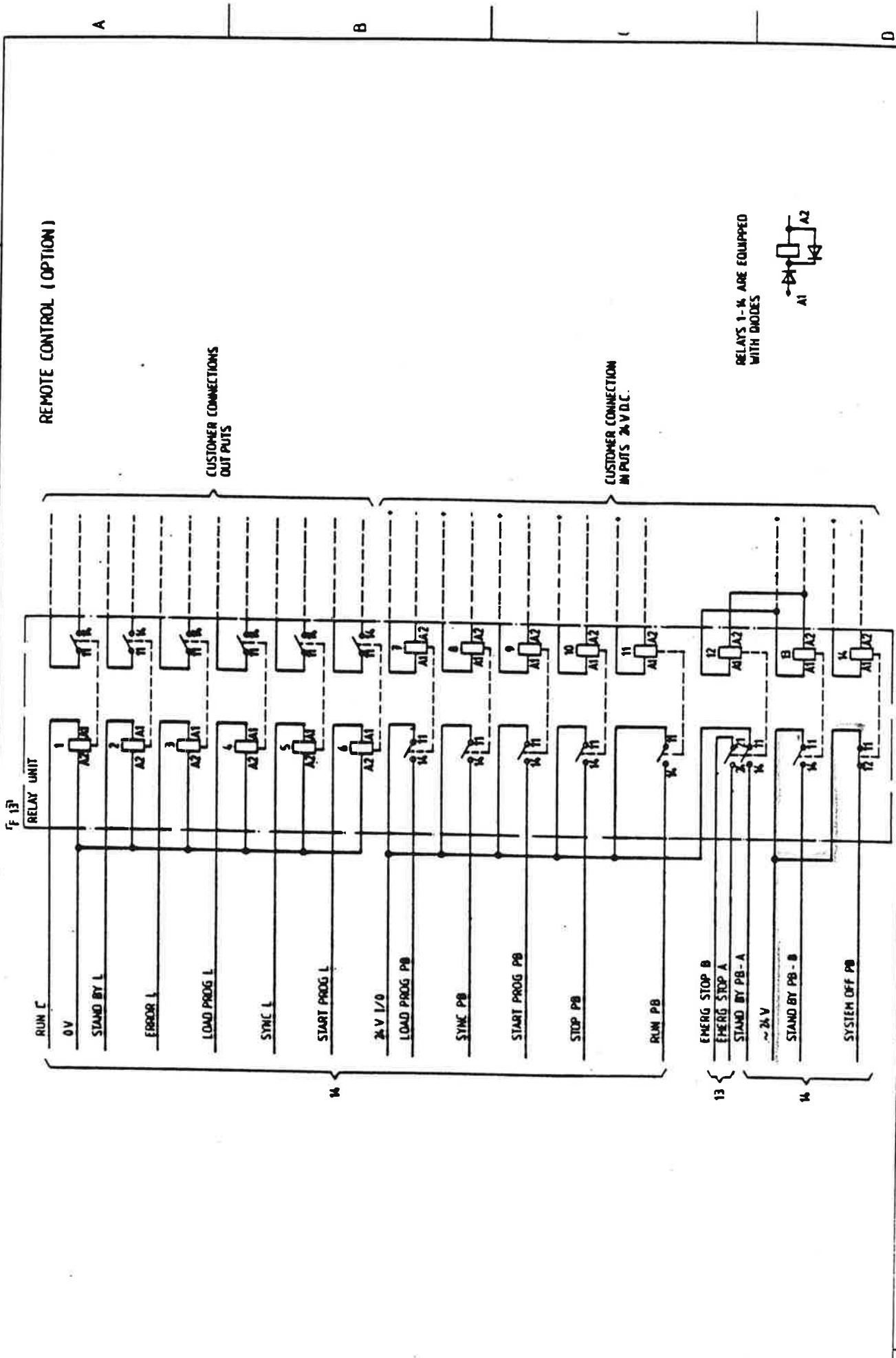


- 1) STRAPS ARE CONNECTED ON DELIVERY SHALL BE REMOVED WHEN F12 1.A1 AND F12 1.A2 ARE CONNECTED
- 2) ON F12 4 CONNECTIONS POINTS 11, 12 AND 14 ARE RESERVED FOR INTERNAL USE CONNECTIONS POINTS 21, 22 AND 24 ARE FREE FOR CUSTOMER USE



RELAYS 1-4 ARE EQUIPPED WITH DIODES

6	Conn 22, 24, F12 4, ch	85 31	Checked by LINDOVIST Checked by JKEM Drawn by QUICK/ITS	CIRCUIT DIAGRAM CONTROL SYSTEM IRB 90S/2 ASEA	6704 100-BEA
4	SH. ADDED	50, 84, 19			
	R 252				
0017 0320 AA (A3) Rev: 0000					



Design checked by	For test Dept	For test Dept	For test Dept	For test Dept	For test Dept
Drawn by	Checked by	Approved by	Project No.	Rev. No.	Sheet No.
5	Remote control ch.	RI 495	84 50	50 84 19	157
4	SH ADD.		84 19	50 84 19	16
Circuit Diagram			IRB 90S/2		
Control System			JKCP 84 19		
Quick/ITS			6704 100-BEA		
LINDQVIST			The Lindqvist Group		
JKEM			JKCP 84 19		
QUICK/ITS			JKCP 84 19		
6817 533W AA (A1) Rev. 11.83					

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Radkorf

Blockerar drifdon
Så att ingen signal
innan bromsar öppna.
Block är endast
ink. på axlar med
broms.

ROBOT

70 V TMR (var)
100 V TMR (var)

DRIVE UNITS AND REACTORS
FOR AXIS 1,2,3

'H17' REACTOR UNIT
Frigoerdynamisk broms
vilken släpps om på
mek. broms fjärrs

'H24' POWER UNIT

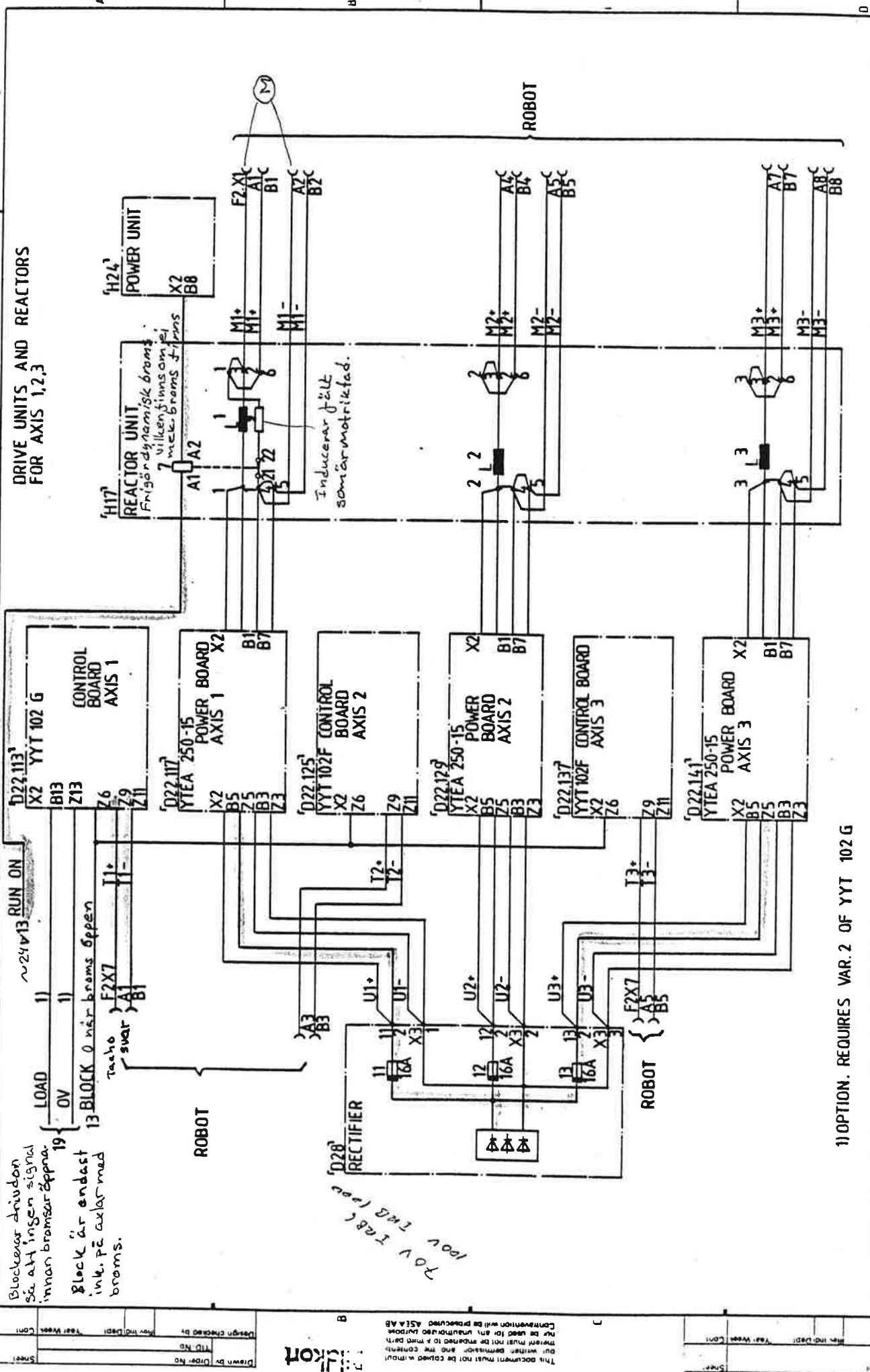
'D26' RECTIFIER

Inducerat fält
som är motriktad.

ROBOT

ROBOT

1) OPTION. REQUIRES VAR. 2 OF YYT 102 G



Design checked by:	Year Week:	16	
Drawn by:	Year Week:	17	
Design checked by:	Year Week:		
Drawn by:	Year Week:		
Description: LINDDQVIST Drawing: JHEM Title: QUICKT/S			
CIRCUIT DIAGRAM CONTROL SYSTEM IRB 90S/12 6704 100-BEA JKCP 83 20			
5	H17 A1, A2 was 91, 92	R1 694	B5 08
5	SH BEORAWIN, YTT 102 G WITH SA R 255	SQ 18	B5 18
App'd Year Week		2	

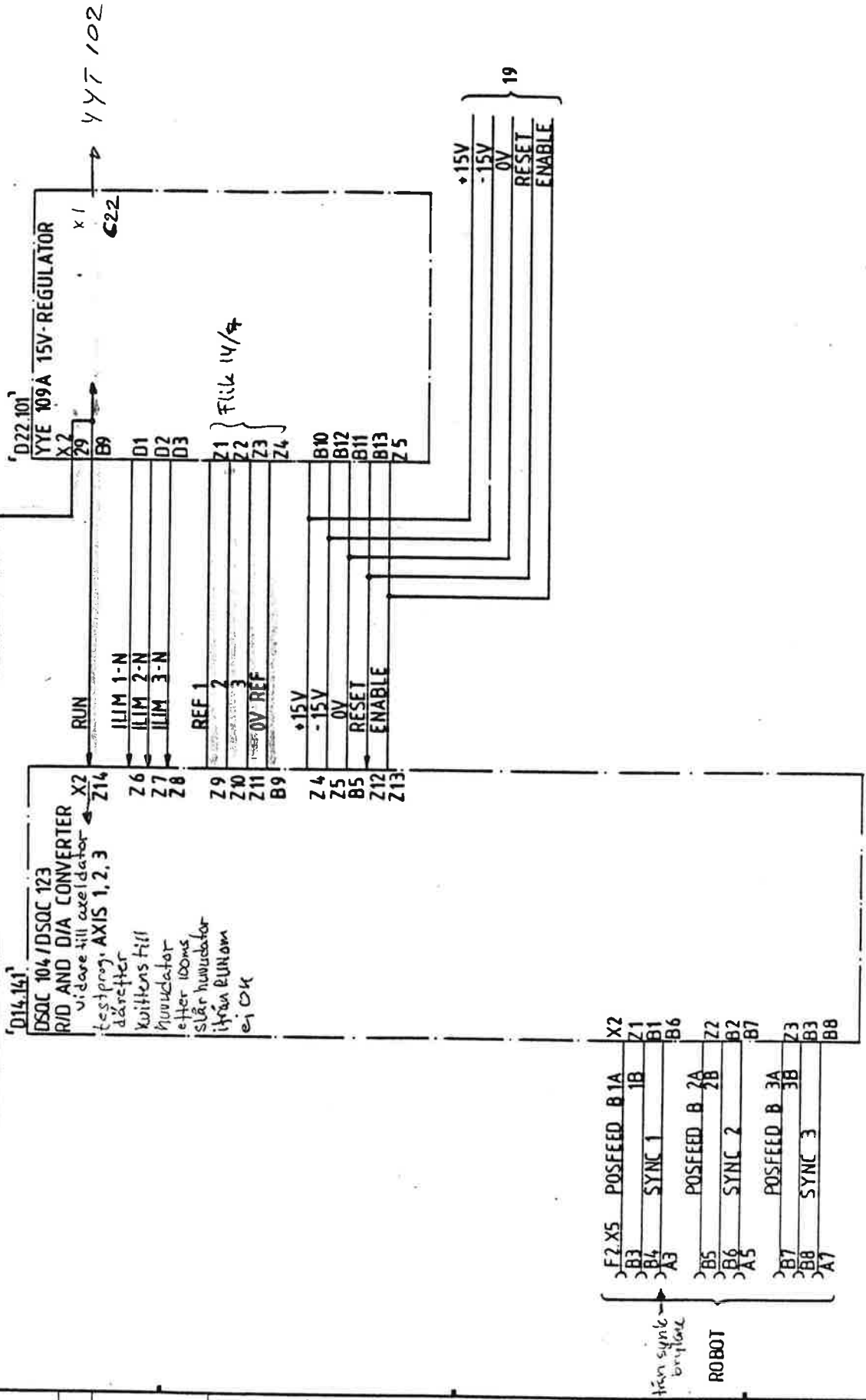
Design checked by: Year Week: Cont

SKOT
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Design checked by: Year Week: Cont

R/D AND D/A CONVERTER FOR AXIS 1,2,3

13 RUN



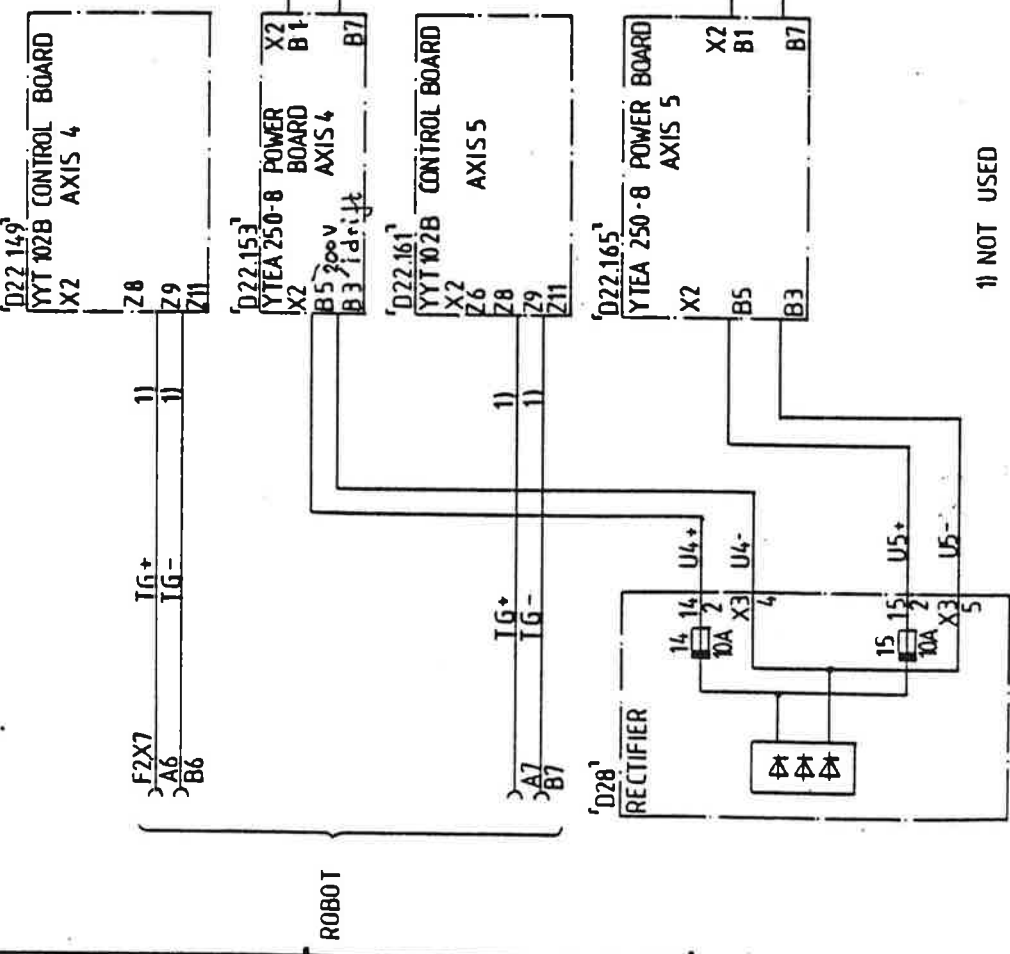
6
5
4
3
2
1

Design checked by LINDQVIST	Perfor. Sheet 17
Drawing checked by JKEM	Perfor. Sheet 18
Drawn by QUICK/T/S	Cont.
Project No. 6704 100 - BEA	
Circuit Diagram CONTROL SYSTEM IRB 20S/2	
ASEA JKCP 83 20	
Applied Year Week 7	
SQ 84 19	
RI 481	
7 84 50	
5 D50C 123 Infr.	
4 SHEET REDRAWN	
3 The Ind. Revision	
0917 6238 AA (A3) Revit:.....	

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177
ASEA

DRIVE UNITS AND REACTORS FOR AXIS 4,5



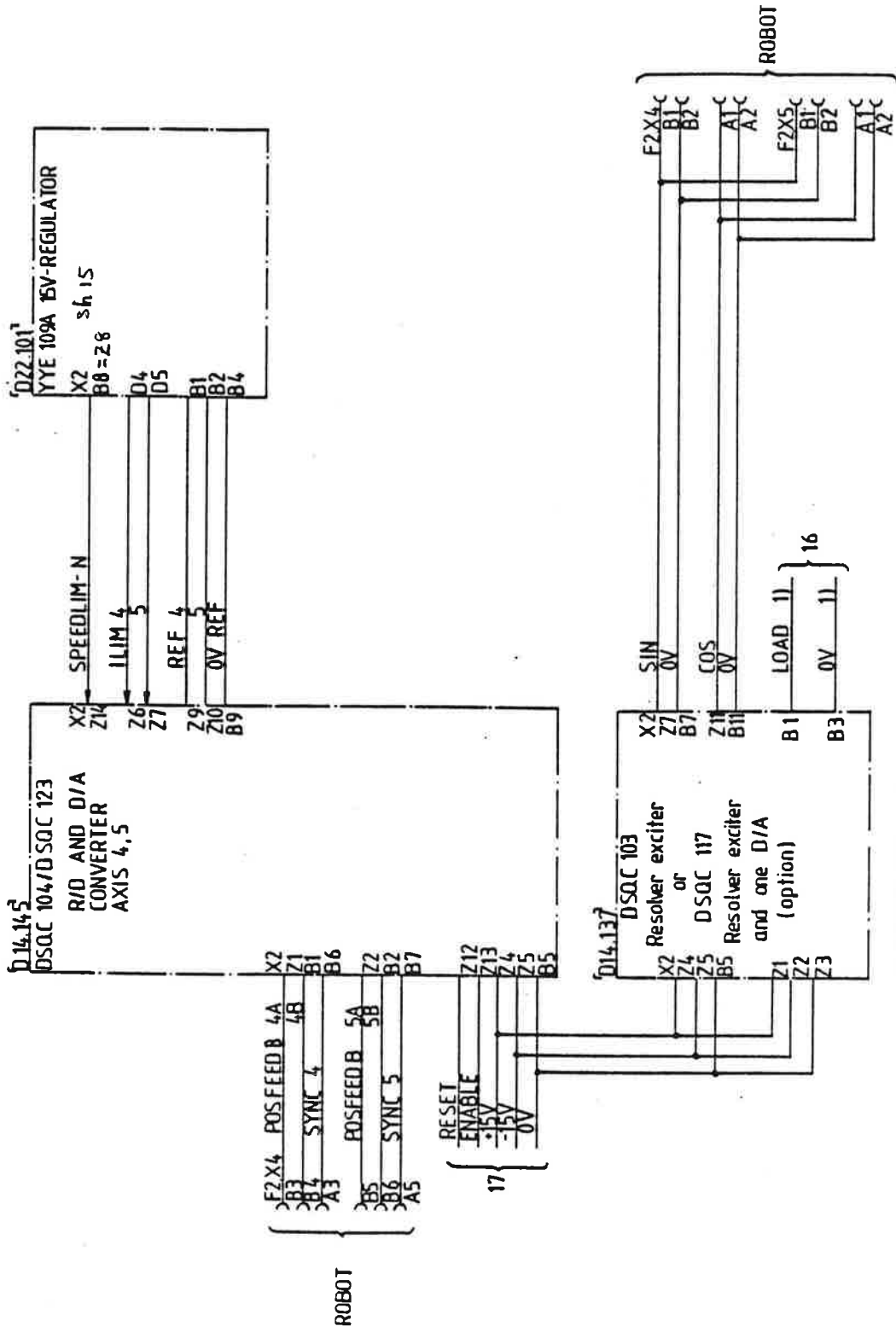
1) NOT USED

Drawn By: [Blank]	Form No: [Blank]	Sheet: [Blank]
Design Checked By: [Blank]	Year/Week: [Blank]	Cont: [Blank]
TID No: [Blank]	Rev No: [Blank]	Year/Week: [Blank]
Order No: [Blank]	Year/Week: [Blank]	Cont: [Blank]
1) SHEET REDRAWN	SQ B4, 19	18
2) SHEET AA (A3) Rev: [Blank]	Appt Year Week	19
Checked by: LINDQVIST	Project: JKLP 83 20	19
Reviewed by: JKEM	Control System IRB 905/2	18
Drawn by: QUICK/TJS	6704 100 - BEA	19
CIRCUIT DIAGRAM		

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skot

R/D AND D/A CONVERTER
FOR AXIS 4,5. RESOLVER FEED

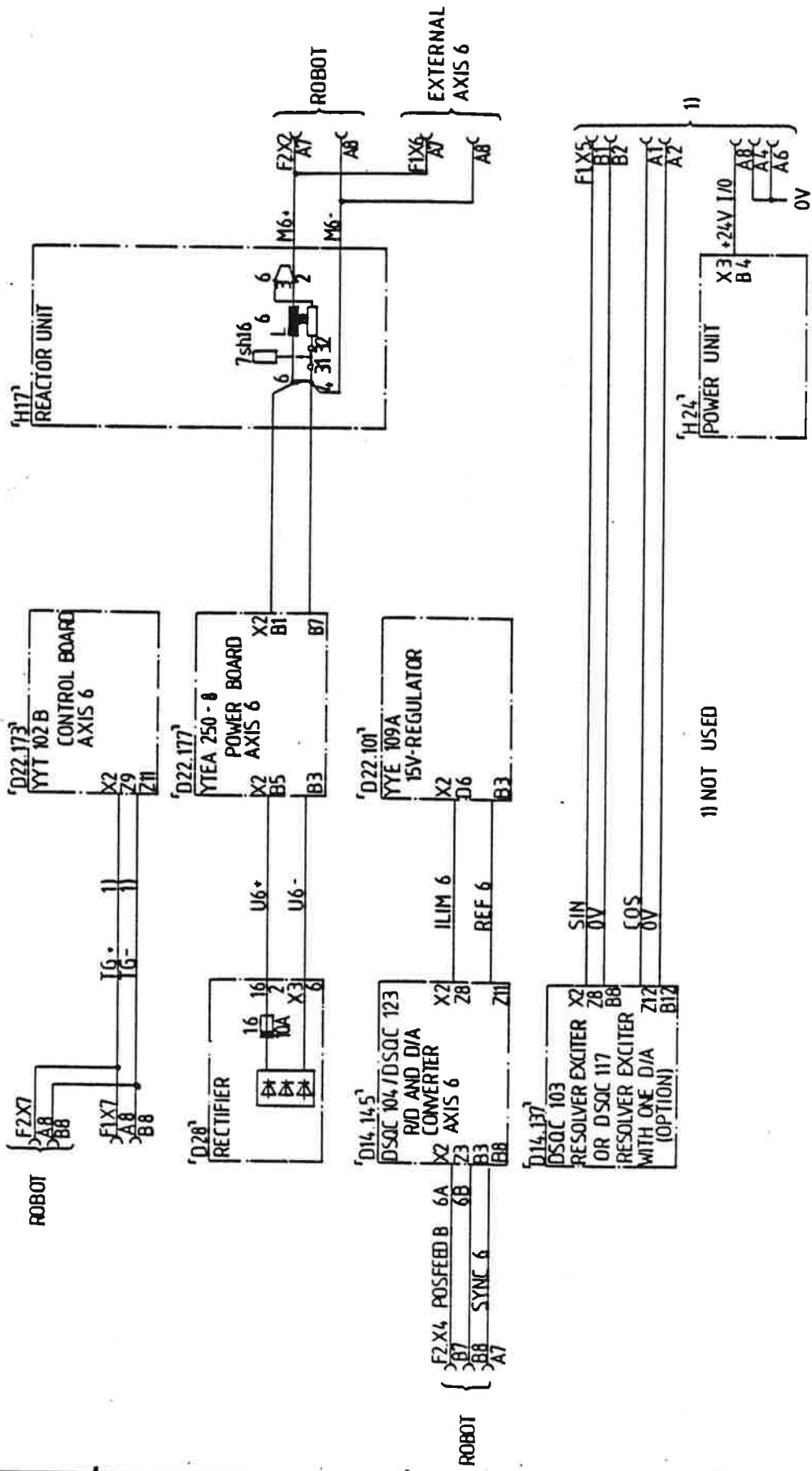


1) OPTION REQUIRES D/SQC 117

DESIGN CHECKED BY: [Signature]	DESIGN BY: [Signature]	DATE: 11/17/81	REV: 1
APPROVED BY: [Signature]	DATE: 11/17/81	REV: 1	
PROJECT: CONTROL SYSTEM IRB 90S/2	CLIENT: 6704 100 - BEA	SHEET: 19	TOTAL SHEETS: 20
DESIGNED BY: LINDQVIST	CHECKED BY: JKEM	DATE: 11/17/81	REV: 1
PROJECT: CONTROL SYSTEM IRB 90S/2	CLIENT: 6704 100 - BEA	SHEET: 19	TOTAL SHEETS: 20
DESIGNED BY: LINDQVIST	CHECKED BY: JKEM	DATE: 11/17/81	REV: 1
PROJECT: CONTROL SYSTEM IRB 90S/2	CLIENT: 6704 100 - BEA	SHEET: 19	TOTAL SHEETS: 20

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AXIS CONTROL FOR AXIS 6
RESOLVER SUPPLY FOR AXIS 7,8,9



1) NOT USED

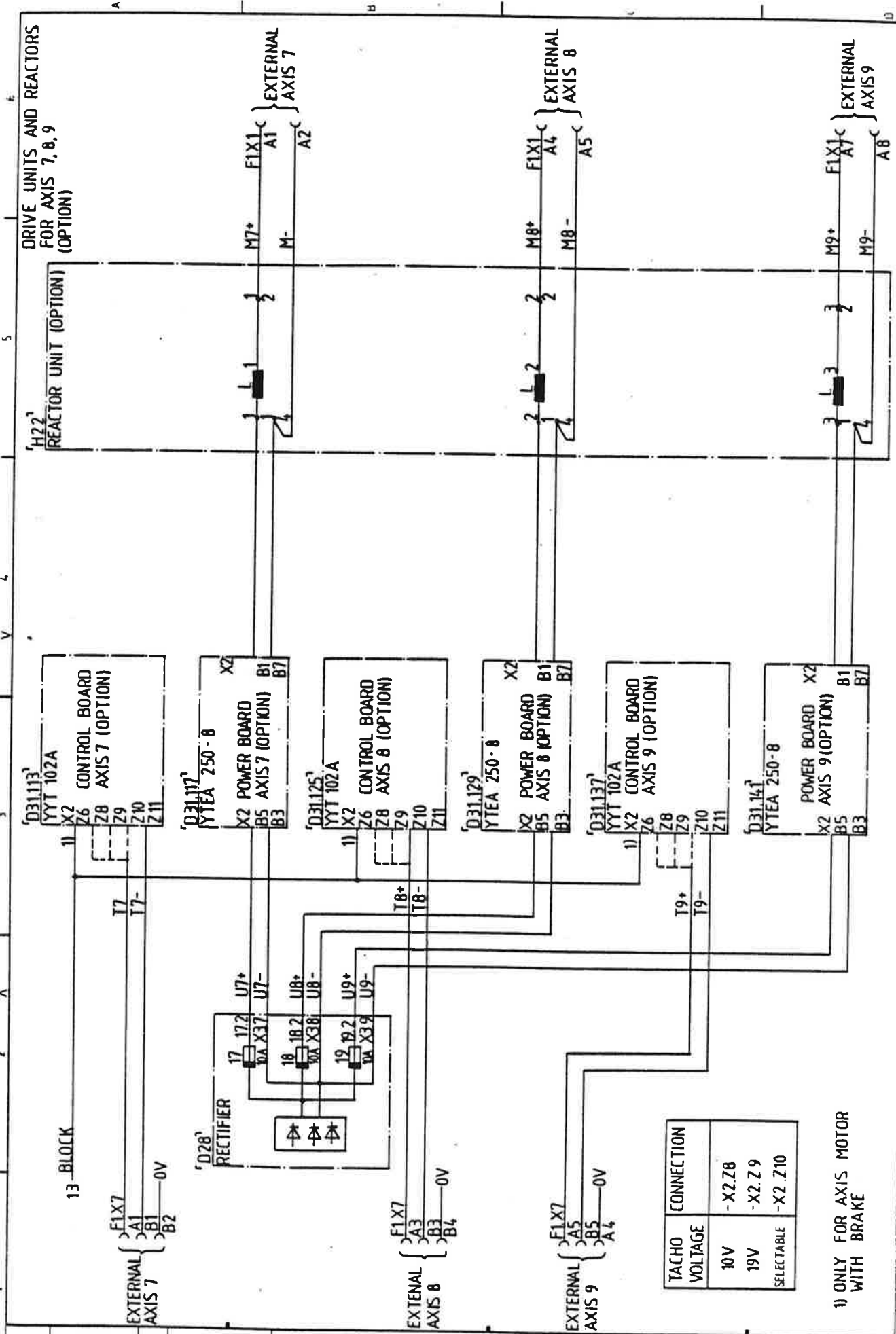
Drawn by:	Year:
Design checked by:	Year:
TID No:	Year:

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Drawn by:	Year:
Design checked by:	Year:
TID No:	Year:

Design checked by: LINDQVIST Drawing checked by: JKEM Checked by: QUICKTIS	CIRCUIT DIAGRAM CONTROL SYSTEM IRB 90S/2 JKIP 83.20	6704 100 - BEA	Rev'd Sheet 20 Cont
5 DSOC 123 inlr H17 adj. RI 181.194	84 50		
4 SH REDRAWN DSOC 117A10 RI 755	50 BL 19		
0017 5319 AA 1A11 Rev 1:	Algal Year Week		



DRIVE UNITS AND REACTORS
FOR AXIS 7, 8, 9
(OPTION)

'H22'
REACTOR UNIT (OPTION)

'D31113'
YYT 102A
CONTROL BOARD
X2
Z6
Z8
Z9
Z10
Z11

'D31117'
YTEA 250-8
POWER BOARD
X2
B5
B3
B1
B7

'D31125'
YYT 102A
CONTROL BOARD
X2
Z6
Z8
Z9
Z10
Z11

'D31129'
YTEA 250-8
POWER BOARD
X2
B5
B3
B1
B7

'D31137'
YYT 102A
CONTROL BOARD
X2
Z6
Z8
Z9
Z10
Z11

'D31141'
YTEA 250-8
POWER BOARD
X2
B5
B3
B1
B7

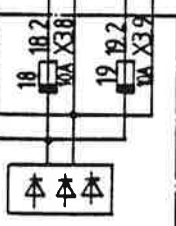
13 BLOCK

EXTERNAL
AXIS 7
FLX7
A1
B1
B2
0V

EXTERNAL
AXIS 8
FLX7
A3
B3
B4
0V

EXTERNAL
AXIS 9
FLX7
A5
B5
A4
0V

'D28'
RECTIFIER



TACHO VOLTAGE	CONNECTION
10V	-X2.Z8
19V	-X2.Z9
SELECTABLE	-X2.Z10

1) ONLY FOR AXIS MOTOR
WITH BRAKE

Drawn by: Order No. TID No. Design checked by: Rev. No. Date: Year Week Cont. Sheet

Design checked by: LINDQVIST
Checked by: JKEM
Drawn by: QUICKTJS

CIRCUIT DIAGRAM
CONTROL SYSTEM IRB 90S/2
JKCP 83 20

6704 100 - BEA

Per Int Sheet: 21
Rev Int Sheet: 22

0317 5319 AA (A3) Rev.1:.....

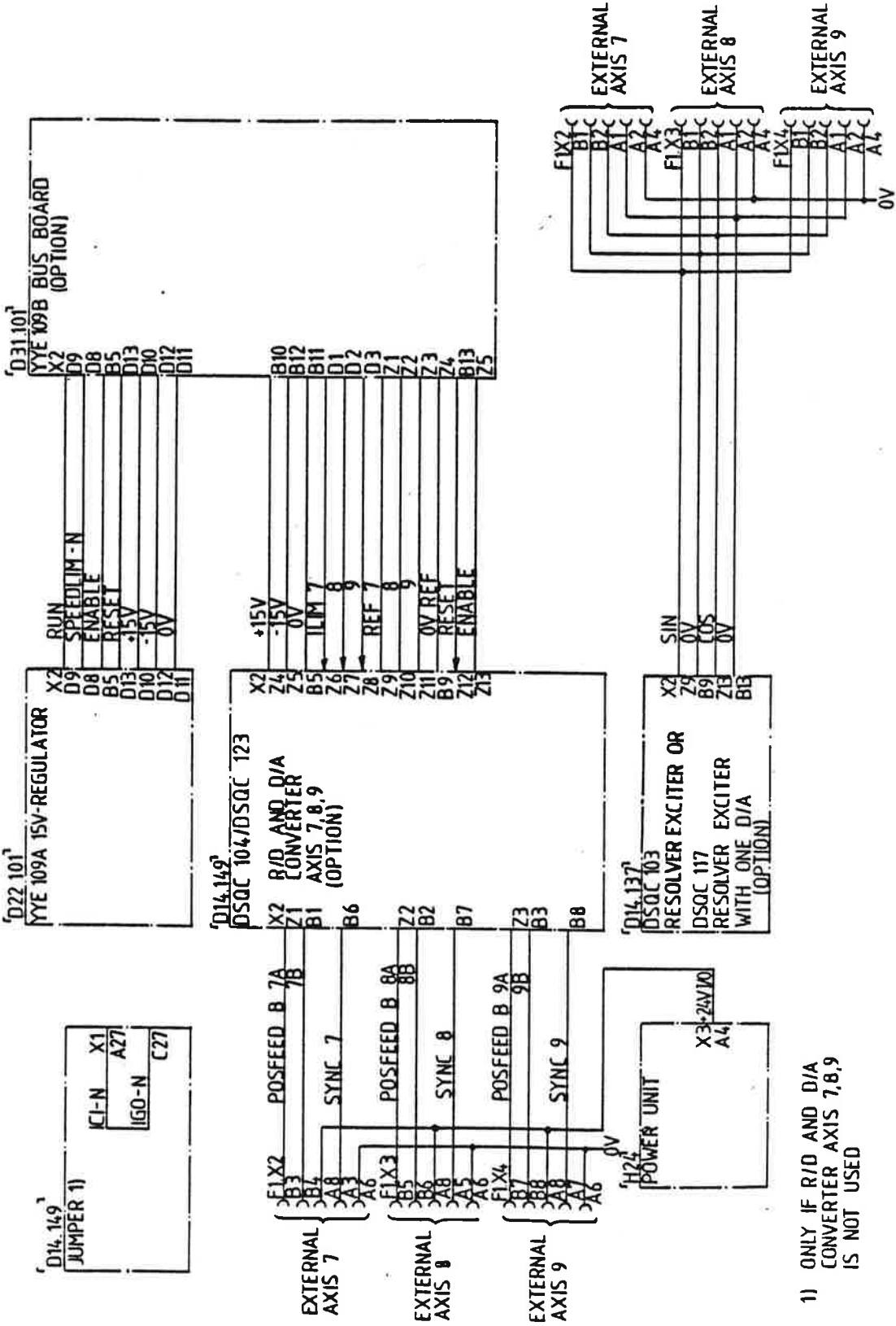
Plickot

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Drawn in form No. Sheet

Design checked by: Year Week Cont. Sheet

R/D AND D/A CONVERTER FOR AXIS 7,8,9
(OPTION)
RESOLVER FEED



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CONVERTER AXIS 7,8,9
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childkot

Drawn by: Erik MC	Year Week: 83 20	Sheet: 22.5
Design checked by: Erik MC	Year Week: 83 20	Rev. No. Sheet: 22
Design checked by: Erik MC	Year Week: 83 20	Cont: 22.5
CIRCUIT DIAGRAM		6704 100 - BEA
CONTROL SYSTEM IRB 90S12		
ASEA		
DESIGN CHECKED BY: J. KEM		
DRAWN BY: J. KEM		
CHECKED BY: J. KEM		
DATE: 1983-03-20		
PROJECT: 6704 100 - BEA		
DRAWING NO: 6704 100 - BEA		
REV. NO.: 22		
SHEET NO.: 22.5		